Reg. Number:	



Continuous Assessment Test (CAT) – I - AUGUST 2025

Programme	:	B.Tech. (CSE and its Specialization)	Semester	:	Fall 2025-2026		
Course Code & Course Title	:	BECE204L & Microcontroller and its Applications	Slot	:	A2+TA2		
Faculty	Dr.Manoj Kumar R Dr.Subhashini.N Dr,Vigneswaran T Dr.Sourabh Paul Dr.Berlin Hency V : Dr,Sivasubramanian A Dr,Vydeki D Dr.Sindhuja Dr.Richards Joe Stanislaus Dr.Suhasini S		Class Number		CH2025260102228 CH2025260102235 CH2025260102242 CH2025260102247 CH2025260102253 CH2025260102257 CH2025260102260 CH2025260102266 CH2025260102268 CH2025260102272		
Duration	:	Dr.Mohamed Aarif K O 90 Minutes		+	CH2025260102277 50		
General Instruct	ion	S:	Max. Mark		30		

Write only your registration number on the question paper in the box provided and do not write other information

Only non-programmable calculator without storage is permitted

		Answer	all questions			
Q. No	Sub Sec.	Description				BT Level
		A company is developing a smar time temperature monitoring, co and a user-friendly interface with requirements, would you recommicroprocessor? Justify your ch processing power, cost, power peripherals.		1	K2	
2		Total value after execuling ear	gram given below, find the register ch line of the program. Assume the H=0AH, D:20H=05H, D:25H=11H Register Content	10	3	K3

3	a)	Find the time delay of the given 8051 assembly language program. Assume the crystal frequency as 12 MHz. [4 Marks]				10	4	К3
		Instr	iction	No. of Machine Cycle				
		MOVI	R1, #250	Gramma Chau and Winnesda and de construction and the Construction and th				
		LABE	L: MOV R2, #250	dis trensissi Anagangsanagabi aki 194 sahari sitangga amanakat maki maki sabadi sitang asal sabada na gasar di I				
		HERE	NOP					
		DJNZ	R2, HERE	2				
		DJNZ	R1, LABEL	2				
		RET		2				
	þ)	Write an 8051 assembly language program to create the delay found in Q. No. 3(a) using Timer 0 in Mode 1. Assume the crystal frequency as 12 MHz. [6 Marks]						
4		Write an 8051 assembly language program to perform multiplication of two sets of 8-bit numbers stored in internal RAM. The first set of 5 numbers begins at memory location 40H, and the second set of 5 numbers begins at 50H. Multiply the corresponding elements from each set, complement each result, and store the complemented products in external RAM starting from address 3000H.					3	K3
5		an LED connected to pressed (logic high simultaneously generally Use interrupts for har	ch is connected to pin P1.2. If the switch is pressed (logic low), D connected to P1.7 should be turned ON. If the switch is not d (logic high), the LED should be turned OFF while aneously generating a square wave in P1.4 with a period 200 µs. terrupts for handling the square wave generation using timer 0 le 2. Assume XTAL frequency as 11.0592 MHz.				4	К3

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